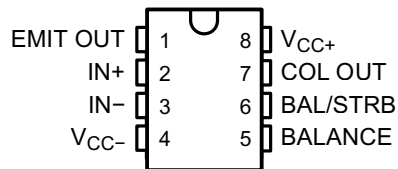
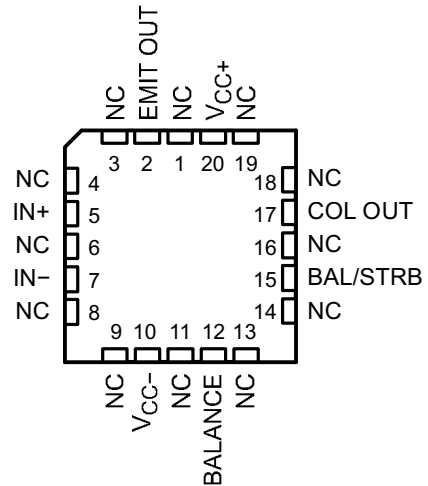


5 Pin Configuration and Functions

D, JG, P, PS, or PW Package
8-Pin SOIC, CDIP, PDIP, SOP or TSSOP
Top View



FK Package
20-Pin LCCC⁽¹⁾
Top View



(1) NC = No internal connection

Pin Functions

PIN			I/O ⁽¹⁾	DESCRIPTION
NAME	SOIC, CDIP, PDIP, SOP, TSSOP	LCCC		
1IN+	2	5	I	Noninverting comparator
1IN–	3	7	I	Inverting input comparator
BALANCE	5	12	I	Balance
BAL/STRB	6	15	I	Strobe
COL OUT	7	17	O	Output collector comparator
EMIT OUT	1	2	O	Output emitter comparator
V _{CC–}	4	10	—	Negative supply
V _{CC+}	8	20	—	Positive supply
NC	—	1	—	No connect (No internal connection)
		3		
		4		
		6		
		8		
		9		
		11		
		13		
		14		
		16		
		18		
		19		

(1) I = Input, O = Output