Unit in mm

TOSHIBA Photocoupler GaAlAs IRED & Photo-IC

TLP250(INV)

Transistor Inverter
Inverters for Air Conditioner
IGBT Gate Drive
Power MOS FET Gate Drive

The TOSHIBA TLP250(INV) consists of a GaAlAs light emitting diode and a integrated photodetector.

This unit is 8-lead DIP.

TLP250(INV) is suitable for gate driving circuit of IGBT or power MOS FET.

Input Threshold Current : I_F=5mA(max)
 Supply Current(ICC) : 11mA(max)
 Supply Voltage(VCC) : 10~35V
 Output Current(IO) : ±2.0A(max)
 Switching Time(tpLH/tpHL) : 0.5µs(max)
 Isolation Voltage : 2500Vrms

UL Recognized : UL1577,File No.E67349

Option(D4)

VDE Approved: DIN EN 60747-5-2 Certificate No.40011913

Maximum Operating Insulation Voltage : $630V_{PK}$

Highest Permissible Over Voltage : 4000VPK

1.2 ± 0.15 0.5 ± 0.11 1.2 ± 0.15 0.5 ± 0.11 1.2 ± 0.15 1.2 ± 0.15 1.2 ± 0.15 1.2 ± 0.15 1.2 ± 0.15 1.2 ± 0.15 1.2 ± 0.25 1.2 ±

Weight: 0.54 g (typ.)

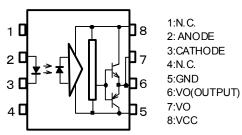
Note: When a EN 60747-5-2 approved type is needed, Please designate the "Option(D4)"

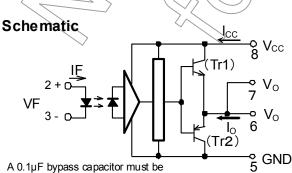
Creepage Distance : 6.4mm(MIN)
 Clearance : 6.4mm(MIN)

Truth Table

Input LED ON ON OF	2		Tr 1	$\langle \langle$		
IIIput LLD	F	OFF	ON	ON	Input LED	
	1	ON	OFF	OFF	IIIput LLD	

Pin Configuration (top view)





Connected between pin 8 and 5(See Note 5).

Absolute Maximum Ratings (Ta=25°C)

	Chara	Symbol	Rating	Unit	
	Forward Current	I _F	20	mA	
	Forward Current Derating (Ta≥	ΔI _F /ΔTa	-0.36	mA /°C	
LED	Peak Transient Forward Currer	I _{FPT}	1	Α	
	Reverse Voltage	V_R	5	V	
	Junction Temperature	Tj	125	°C	
	"H" Peak		-1.5) >	
	Output Current	Iz (Note 2)	I _{OPH}	=2.0	A
	"L" Peak	Iz (Note 2)		+1.5	Α
١.	Output Current	I _{OPL}	+2.0	A	
Detector	Output Voltage	Vo	<u>)</u> 35	V	
Dete	Output Voltage		24		
	Supply Voltage	(Ta≤70°C)	Vcc	35	XX
	- Tappy Tanaga	γ / \sim	24		
	Output Voltage Derating (Ta≥7)	_∕Δν₀/ΔΤα	_0.73	(V)/°C	
	Supply Voltage Derating (Ta≥7)	ΔV _{cc} /ΔTa	-0.73	V/PC/	
	Junction Temperature	⟩ T _j	125	\\c\	
Оре	erating Frequency	f	(25	к̀Нz	
Оре	erating Temperature Range	T _{opr}	-20~85	∕ °C	
Stor	rage Temperature Range	T _{stg}	∕ - 55 ² 125	°C	
Lea	d Soldering Temperature(10s)	Tsok	260	°C	
Isola	ation Voltage (AC,1min., R.H. s	BVs	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- (Note 1) : Pulse width PW≤1µs,300pps
- (Note 2): Exporenential Waveform
- (Note 3) : Exporenential Waveform $I_{OPH} \le -1.0A (\le 2.5 \mu s)$, $I_{OPE} \le +1.0A (\le 2.5 \mu s)$
- (Note 4): Device considerd a two terminal device: pins 1,2,3 and 4 shorted together and pins 5,6,7 and 8 shorted together.
- (Note 5): A ceramic capacitor(0.1µF) should be connected from pin 8 to pin 5 to stabilize the operation of the high gain linear amplifier. Failure to provide the bypassing may impair the switching proparty. The total lead length between capacitor and coupler should not exceed 1cm.

Recommended Operating Conditions

Characteristics		Symbol	Min	Тур.	M	ax	Unit
Input Current, ON	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	I _{F (ON)}	7	8	1	0	mA
Input Voltage, OFF		V _{F (OFF)}	0	_	0.8		V
Supply Voltage		V _{CC}	15	_	30	20	V
Peak Output Current		I _{OPH} / I _{OPL}	_	_	±0.5		Α
Operating Temperature		T _{opr}	-20	25	70	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

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Electrical Characteristics (Ta = -20~70°C, Unless otherwise specified)

Characteris	tics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input Forward Voltage		V _F	_	I _F = 10 mA, Ta = 25°C	_	1.6	1.8	V
Temperature Coefficier Forward Voltage	nt of	ΔV _F /ΔTa	_	I _F = 10 mA	-	-2.0	-	mV /°C
Input Reverse Current	nput Reverse Current		_	V _R = 5 V, Ta = 25°C	Á	_	10	μA
Input Capacitance	_	C_T	1	V = 0, f = 1 MHz, Ta = 25°C		45	250	pF
Output Current	"H" Level	I _{OPH}	2	$V_{CC} = 30 \text{ V}$ $I_F = 10 \text{ mA}$ $V_{8-6} = 4 \text{ V}$) [.0	-1.5	1	Α
Output Guirent	"L" Level	I _{OPL}	1	(*1) $I_F = 0$ $V_{6-5} = 2.5 \text{ V}$	1.0	2	-	A
Output Voltage	"H" Level	V _{OH}	3	$V_{CC1} = +15 \text{ V}$ $V_{EE1} = -15 \text{ V}$ $R_L = 200\Omega, I_F = 5 \text{ mA}$	11	12.8	ı	V
Output Voltage	"L" Level	V _{OL}	4	$V_{CC1} = +15 \text{ V}$ $V_{EE1} = -15 \text{ V}$ $R_L = 200 \Omega, V_F = 0.8 \text{ V}$	- (-14.2	-12.5	V
Supply Current	"H" Level	Іссн	_	$I_{F} = 10 \text{ mA}$ $Ta = 25^{\circ}\text{C}$ $I_{F} = 10 \text{ mA}$			11	mA
Supply Current	"L" Level	I _{CCL}	7	I _F = 0 mA Ta = 25°C	9	7.5	_ 11	mA
Threshold Input Current	L→H	I _{FLH}		V _{CC1} = +15 V V _{EE1} = -15 V R _L = 200Ω, V _Q > 0V)	1.2	5	mA
Threshold Input Voltage	H→L	V _{FHL}		$V_{CC1} = +15 \text{ V}$ $V_{EE1} = -15 \text{ V}$ $R_L = 200\Omega, V_O < 0V$	0.8	_	_	V
Supply Voltage		(V _{cc}) —		10	_	35	V
Capacitance (Input-Ou	tput)	Cs	_	V _s = 0, f = 1 MHz, Ta = 25°C	_	1.0	2.0	pF
Resistance (Input-Outp	out)	Rs	- (V _S = 500 V; Ta = 25°C R.H.≤60%	1×10 ¹²	10 ¹⁴	_	Ω

(*) : All typical values are at Ta=25°C

(*1) : Duration of IO time ≤ 50µs



Switching Characteristics ($Ta = -20 \sim 70^{\circ}$ C,Unless otherwise specified)

Characteris	tics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Propagation	L→H	t _{pLH}			0.05	0.15	0.5	
Delay Time	H→L	t _{pHL}		I _F = 8 mA,	0.05	0.15	0.5	
Switching Time Dispersion between ON and OFF		tpHL-tpLH	5	$V_{CC} = 15 \text{ V}$ $R_L = 20\Omega, C_L = 10 \text{nF}$		_	0.45	μs
Output Rise Time		t _r			(F) /	_	
Output Fall Time		t _f			7/^		_	
Common Mode Transient Immunity at High Level Output CM _H		СМн	- 6	V _{CM} = 1000 V, I _F = 8 mA V _{CC} = 30 V, Ta = 25 °C	-15000	_	_	V /µs
Common Mode Transient Immunity at Low Level Output CML		J	V _{CM} = 1000 V, I _F = 0 mA V _{CC} = 30 V, Ta = 25°C	15000	_	-	V /µs	

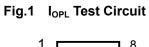


Fig.2 IOPH Test Circuit

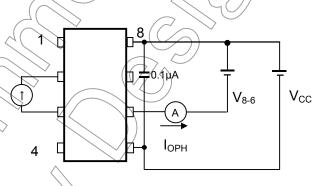
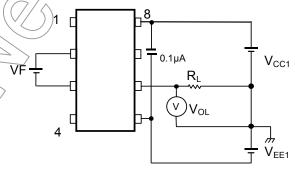


Fig.3 V_{OH} Test Circuit

Fig.4 Vol Test Circuit



 V_{EE1}

Fig.5 tpLH, tpHL, tr, tf Test Circuit

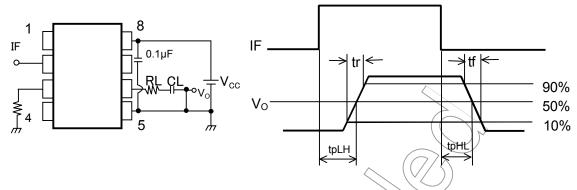
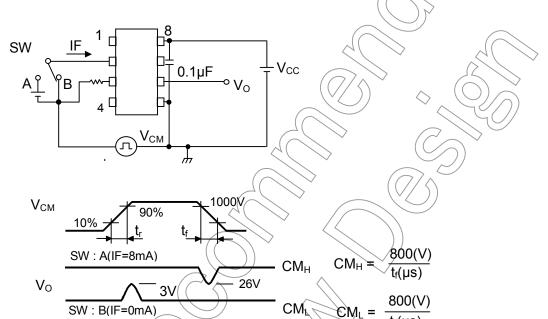
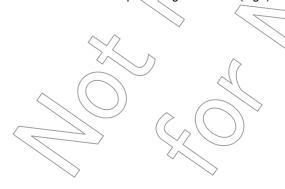


Fig.6 CM_H, CM_L Test Circuit



CML(CMH) is the maximum rate of rise(fall) of the common mode voltage that can be sustained with the output voltage in the low(high)state.



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