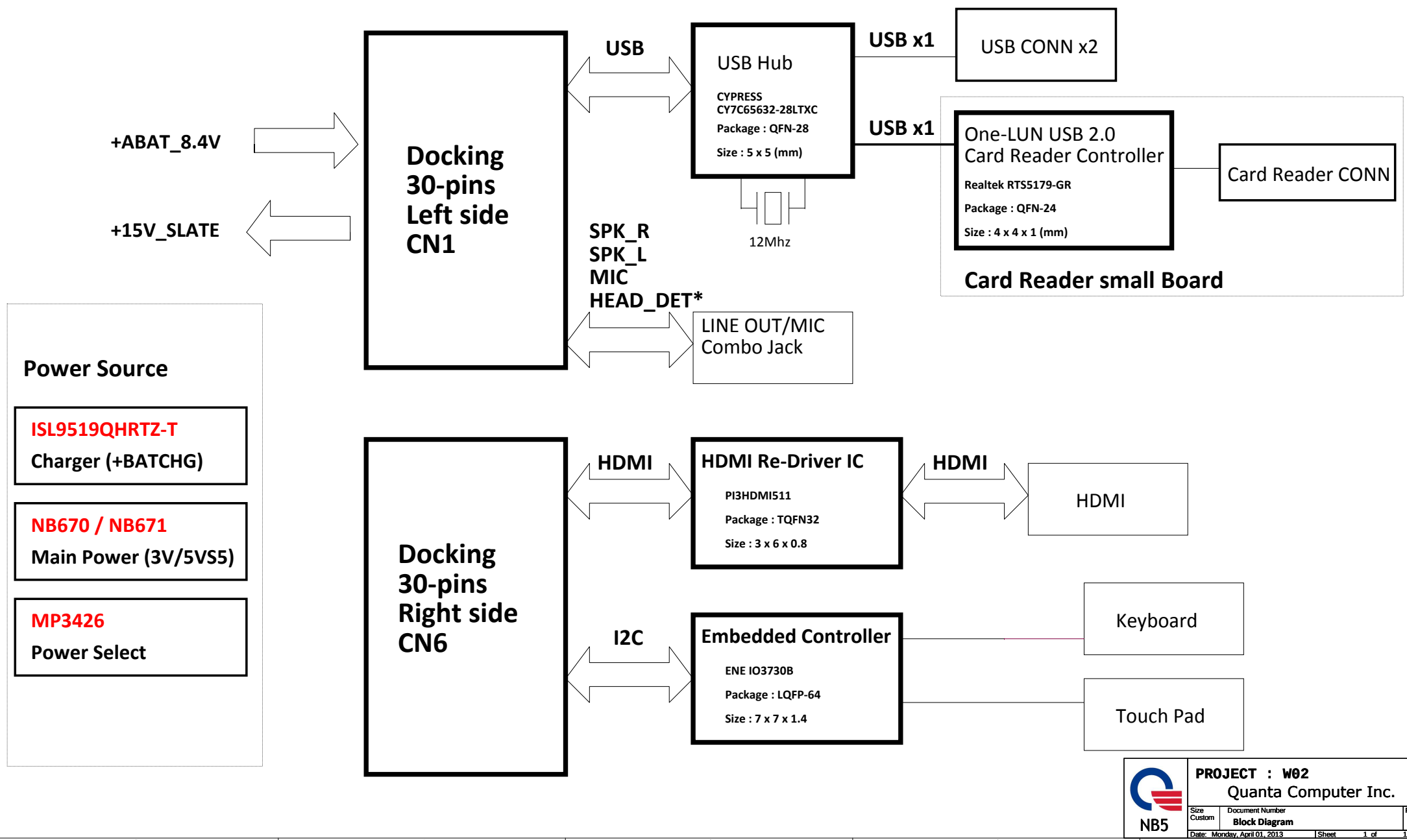
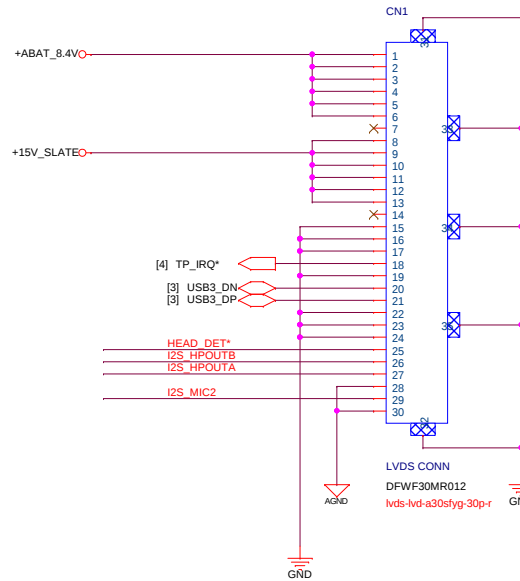


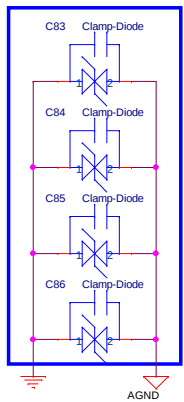
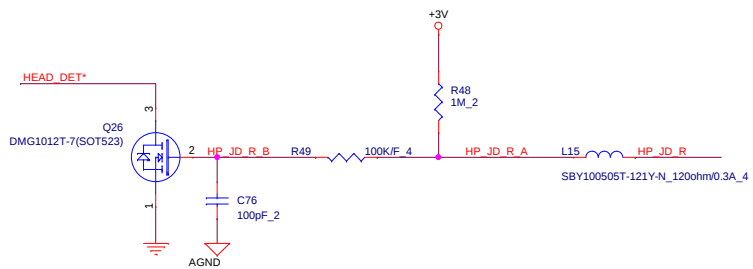
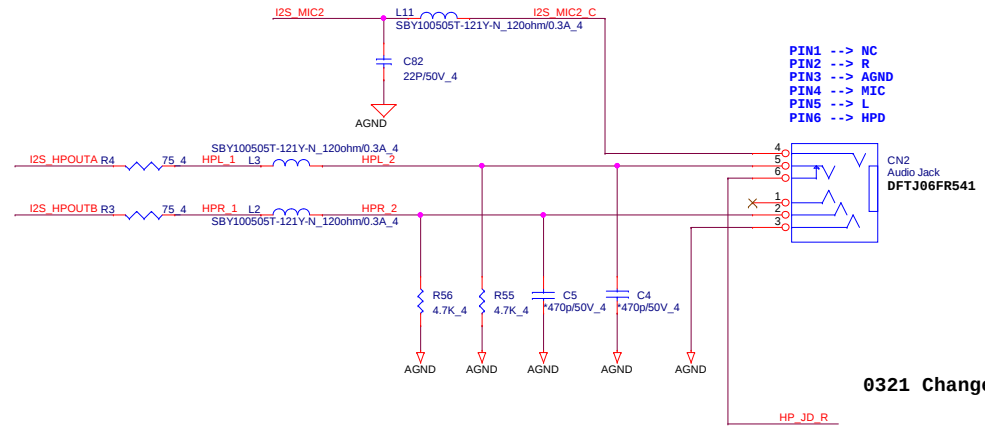
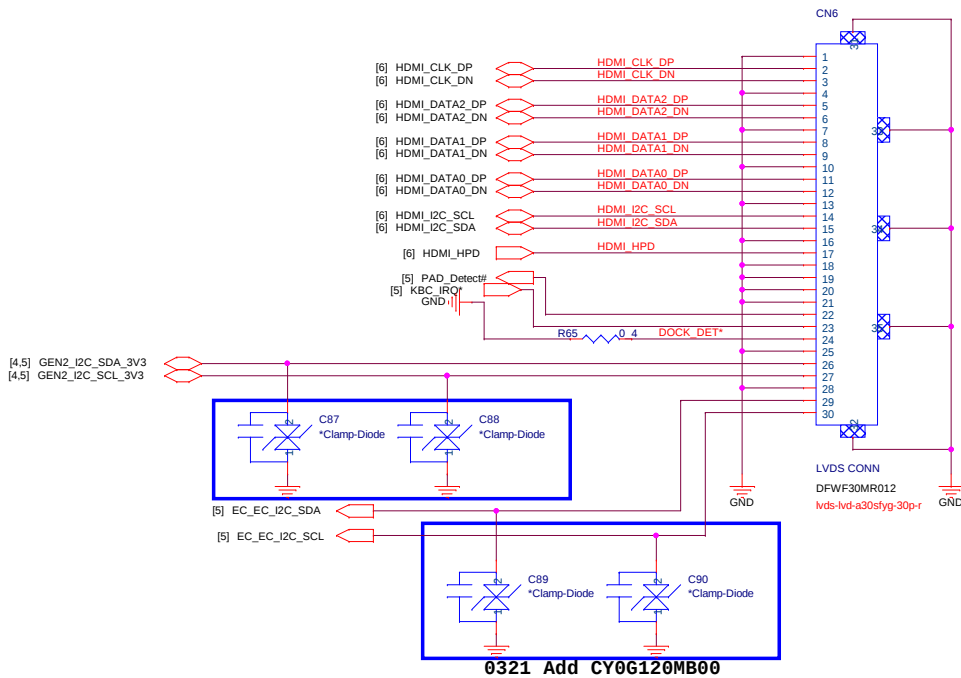
Maya Tegra4 (T40k) Platform Base Block Diagram



Docking (Left) Audio, USB, G2_I2C



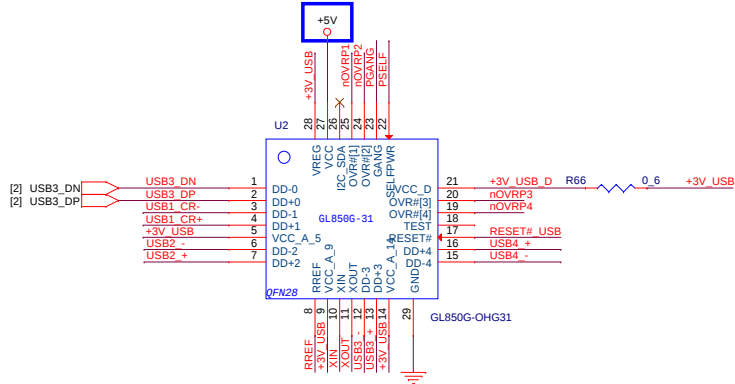
Docking (Right) EC,



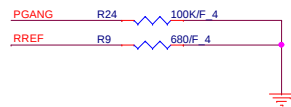
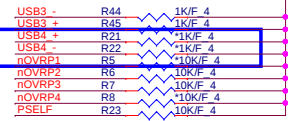
[9] +ABAT_8.4V → +ABAT_8.4V
[9] +15V_SLATE → +15V_SLATE
[3,4,5,6,7,8,9] +3V → +3V

NB5	PROJECT : W92 Quanta Computer Inc.		
	Size Custom	Document Number Audio Jack & Docking conn	Rev 1A
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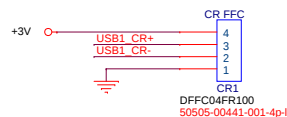
0315 Modify +5V



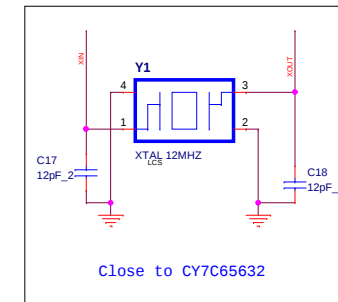
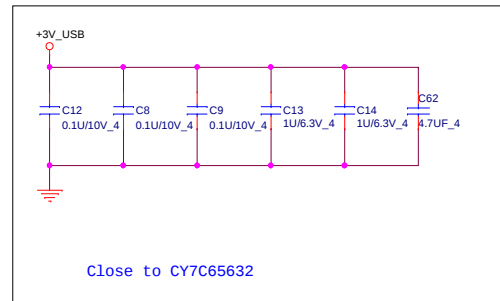
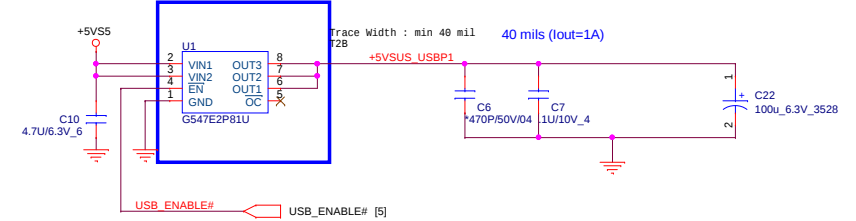
0321 un-stuff R21, R22, R5



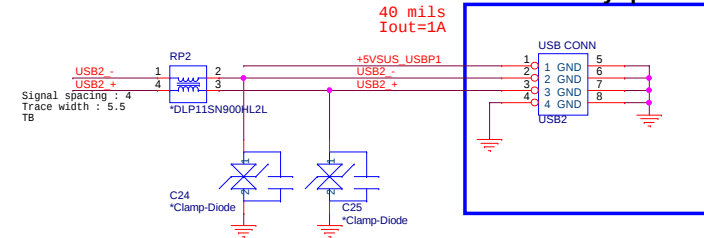
Card Reader FFC Connector



0321 Modify P/N to AL000547002



1228 Modify pin define



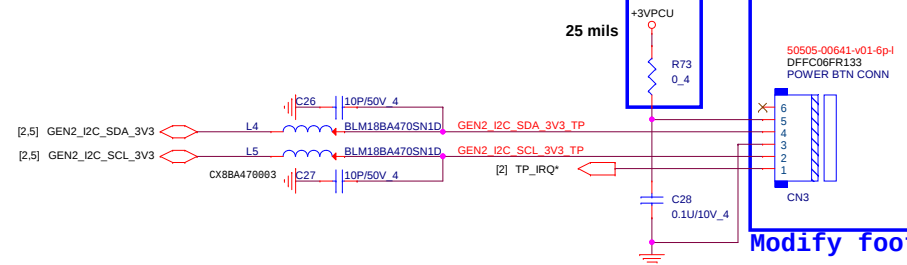
[6,7] +5VSS
[2,4,5,6,7,8,9] +3V



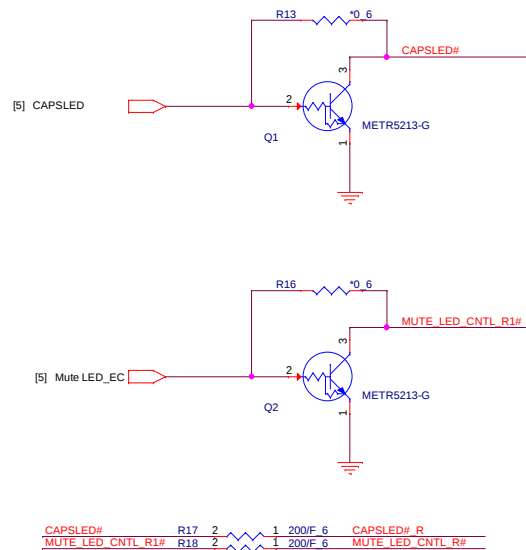
PROJECT : W92
Quanta Computer Inc.

Size Custom	Document Number USB Hub(CY7C65632) / CR FFC CONN	Rev 1A
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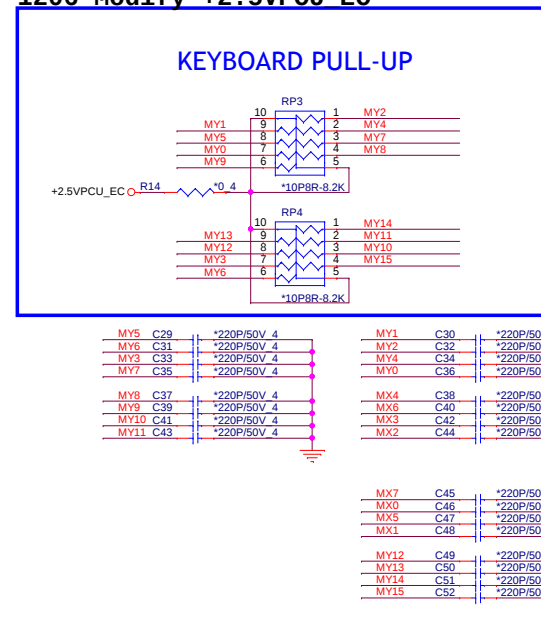
Touch Pad Connector 0315 Modify +3VPCU



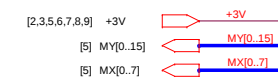
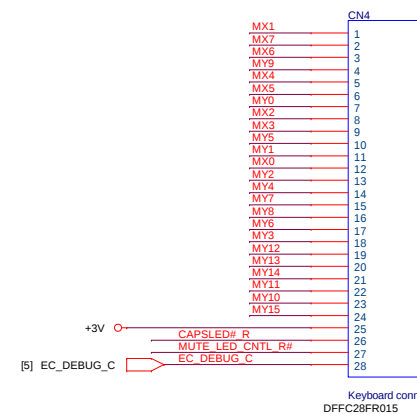
KEYBOARD LED

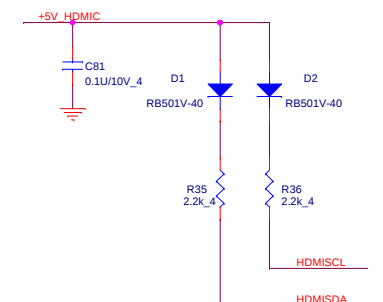
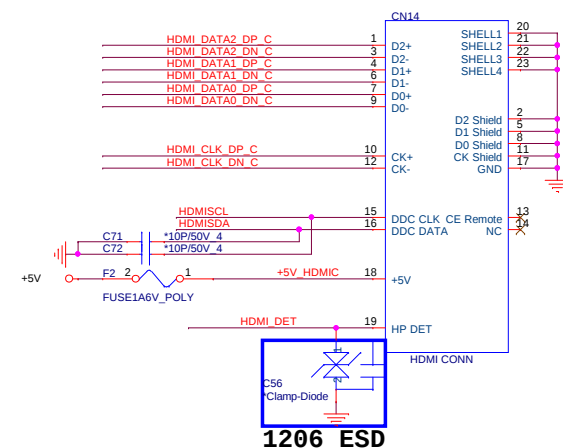
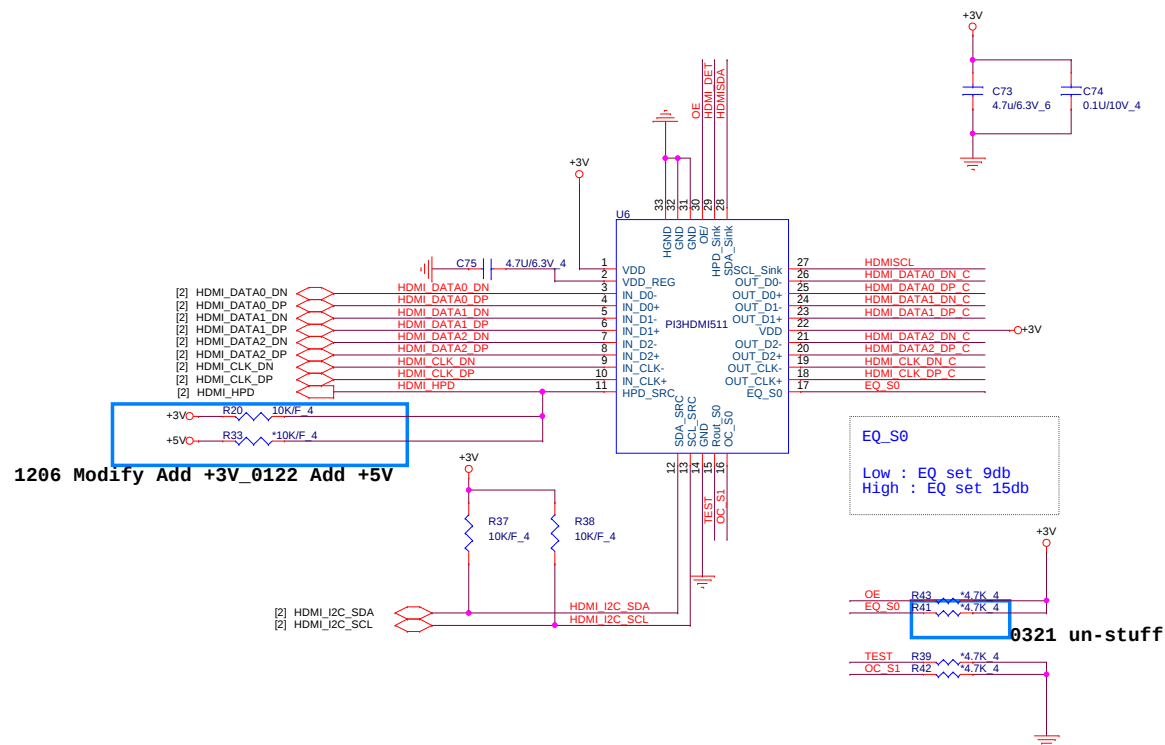


1206 Modify +2.5VPCU_EC



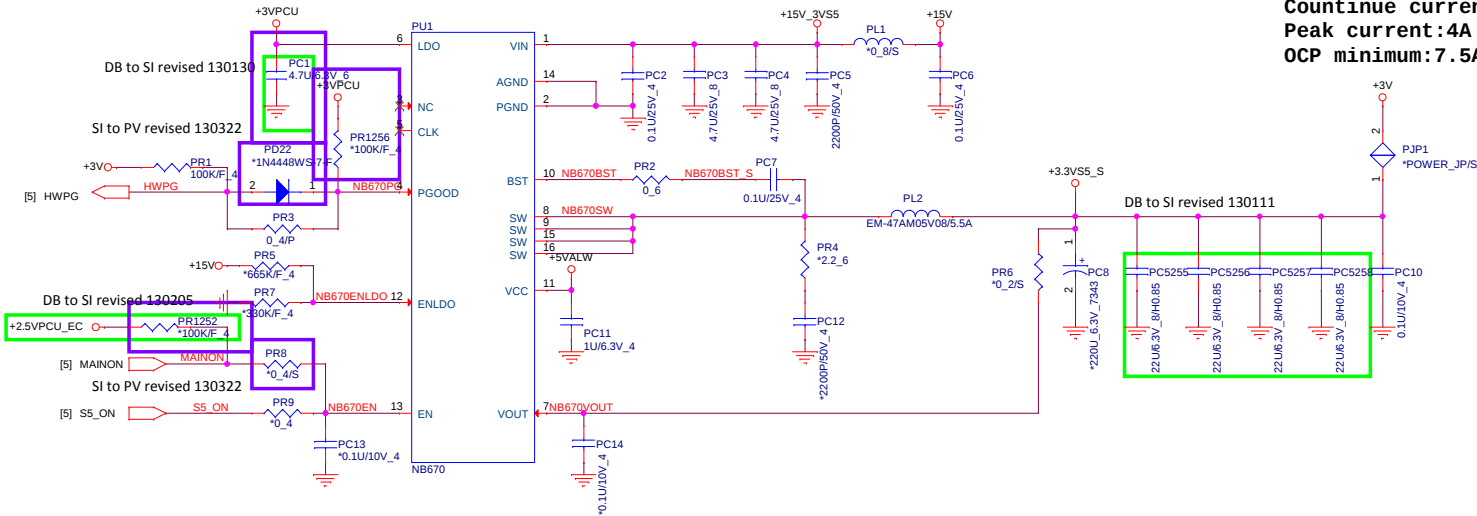
Keyboard



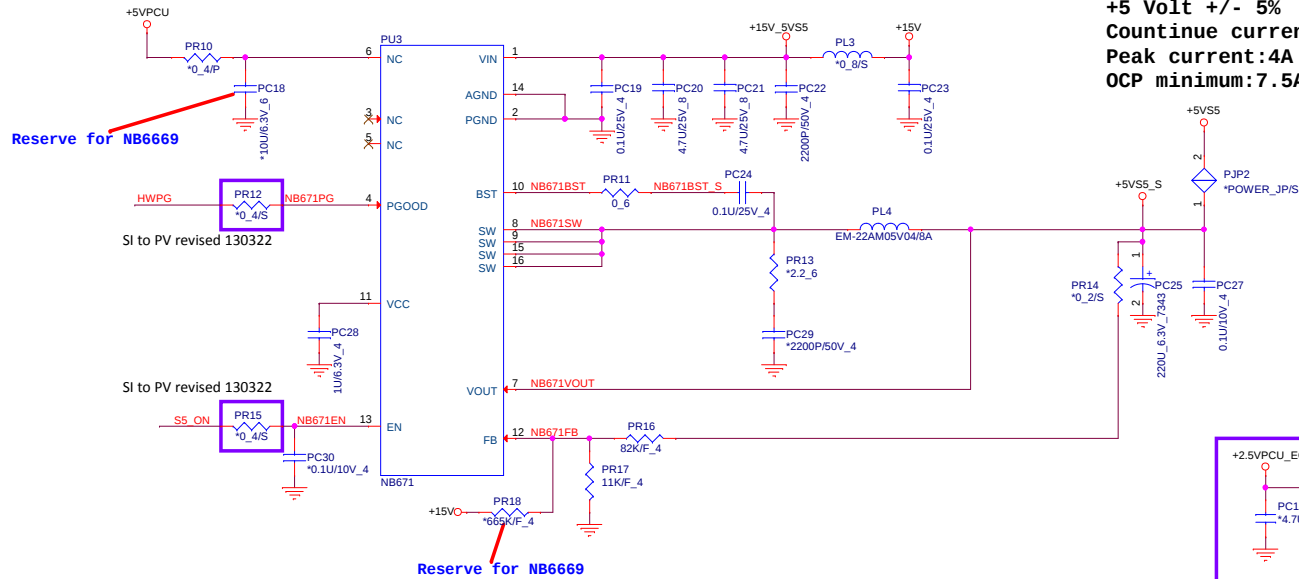
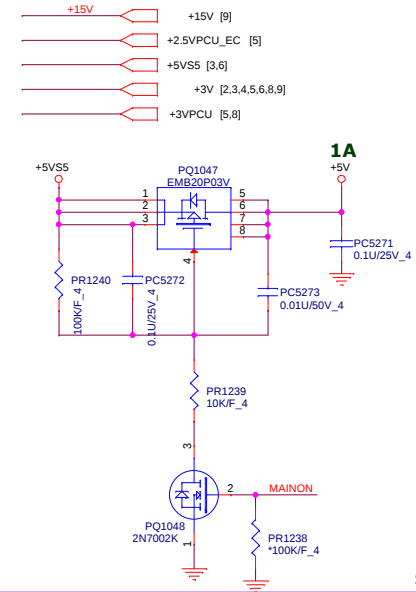


[2,3,4,5,7,8,9] +3V
[3,7] +5VS5

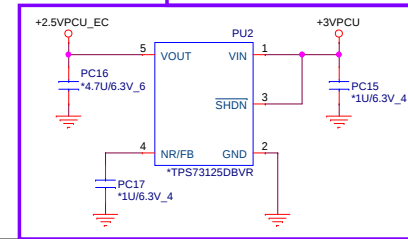
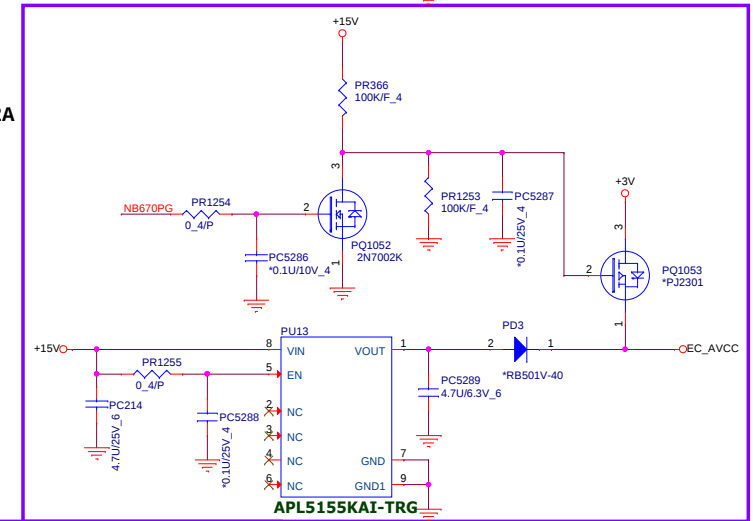
NB5	PROJECT : W92		
	Quanta Computer Inc.		
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	Audio Jack & Docking conn		
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+3.3 Volt +/- 5%
Continue current:2A
Peak current:4A
OCP minimum:7.5A



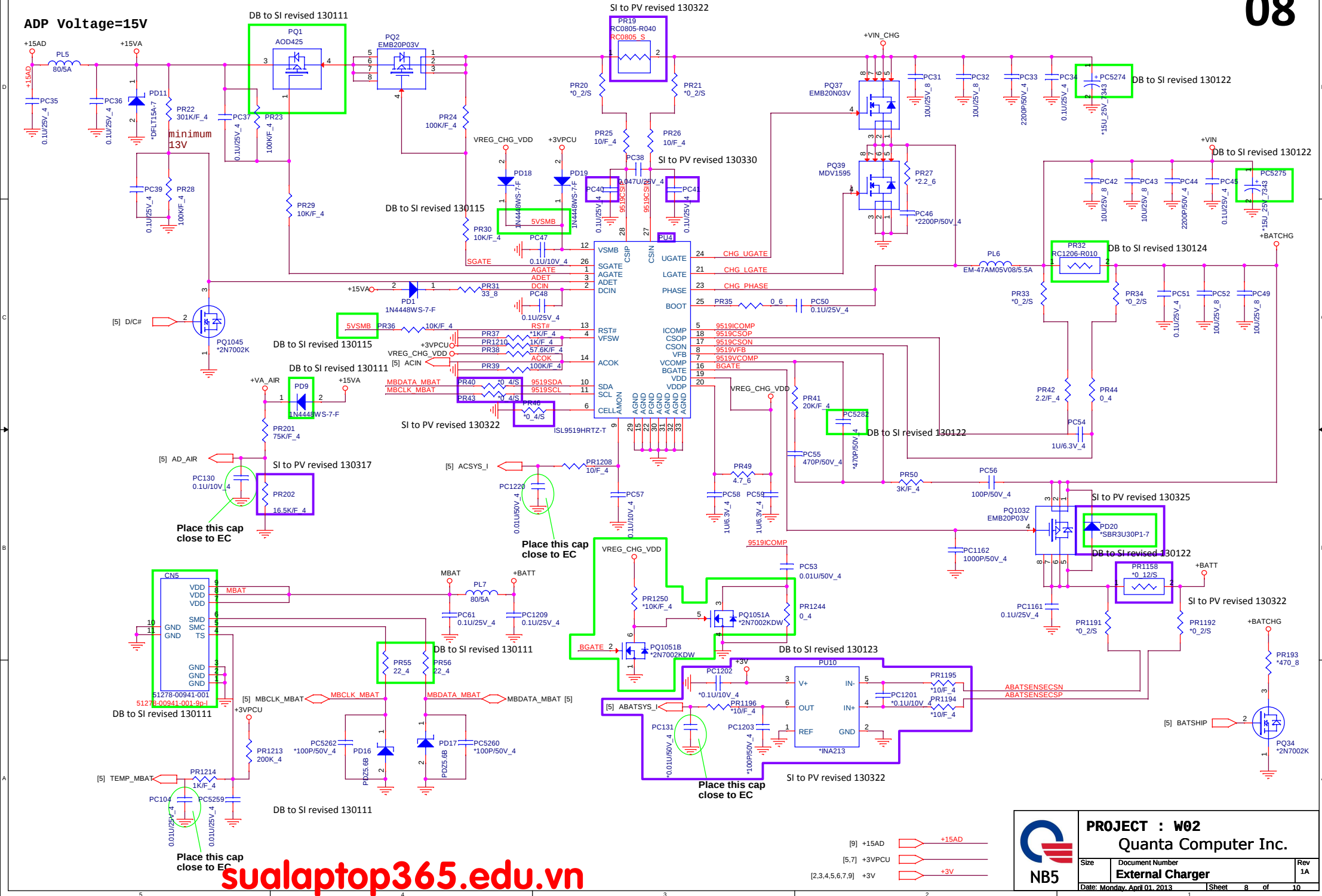
+5 Volt +/- 5%
Continue current:2A
Peak current:4A
OCP minimum:7.5A



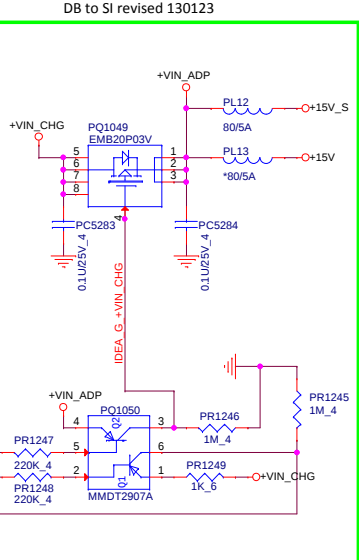
PROJECT : W02
Quanta Computer Inc.

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ADP Voltage=15V



ABAT=6V~8.4V



[8]	+15AD	←	+15AD
[2]	+ABAT_8.4V	→	+ABAT 8.4V
[2]	+15V_SLATE	→	+15V SLATE
[7]	+15V	→	+15V
[2,3,4,5,6,7,8]	+3V	→	+3V

