

ELECTRICAL CHARACTERISTICS (Refer to the Test Circuit $V_S = \pm 40V$, $R_L = 8\Omega$, $R_g = 50\Omega$; $T_{amb} = 25^\circ C$, $f = 1\text{ kHz}$; unless otherwise specified).

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V _S	Supply Range		±12		±50	V
I _q	Quiescent Current			30		mA
I _b	Input Bias Current			0.3	1	μA
V _{OS}	Input Offset Voltage		-10		10	mV
I _{OS}	Input Offset Current				0.2	μA
P _O	RMS Continuous Output Power	d = 1%: R _L = 4Ω; V _S = ± 29V,		80 80		W
		d = 10% R _L = 4Ω ; V _S = ±29V		100 100		W
d	Total Harmonic Distortion (**)	P _O = 5W; f = 1kHz P _O = 0.1 to 50W; f = 20Hz to 15kHz		0.005	0.1	% %
I _{SC}	Current Limiter Threshold			6.5		A
SR	Slew Rate			15		V/μs
G _V	Open Loop Voltage Gain			80		dB
G _V	Closed Loop Voltage Gain (1)			30		dB
e _N	Total Input Noise	A = curve f = 20Hz to 20kHz		1 2	5	μV μV
R _i	Input Resistance		100			kΩ
SVR	Supply Voltage Rejection	f = 100Hz; V _{ripple} = 0.5Vrms		75		dB
T _S	Thermal Protection	DEVICE MUTED		150		°C
		DEVICE SHUT DOWN		160		°C
STAND-BY FUNCTION (Ref: to pin 1)						
V _{ST on}	Stand-by on Threshold				1.5	V
V _{ST off}	Stand-by off Threshold		3.5			V
ATT _{st-by}	Stand-by Attenuation		70	90		dB
I _{q st-by}	Quiescent Current @ Stand-by			0.5		mA
MUTE FUNCTION (Ref: to pin 1)						
V _{Mon}	Mute on Threshold				1.5	V
V _{Moff}	Mute off Threshold		3.5			V
ATT _{mute}	Mute Attenuation		60	80		dB
CLIP DETECTOR						
Duty	Duty Cycle (pin 5)	THD = 1% ; R _L = 10KΩ to 5V		10		%
		THD = 10% ; R _L = 10KΩ to 5V		40		%
I _{CLEAK}		PO = 50W			1	μA
SLAVE FUNCTION pin 4 (Ref: to pin 8 -Vs)						
V _{Slave}	SlaveThreshold				1	V
V _{Master}	Master Threshold		3			V

Note (1): $G_{Vmin} \geq 26\text{ dB}$

Note: Pin 11 only for modular connection. Max external load $1M\Omega/10\text{ pF}$, only for test purpose

Note ():** Tested with optimized Application Board (see fig. 2)